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**Fine ceramics (advanced ceramics, advanced technical ceramics) —
Test method for GaN crystal surface defects — ~~Part 2: Method for
determining etch pit density~~**

**Part 2:
Method for determining etch pit density**

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Foreword

ISO (the International Organization for Standardization) is a worldwide federation of national standards bodies (ISO member bodies). The work of preparing International Standards is normally carried out through ISO technical committees. Each member body interested in a subject for which a technical committee has been established has the right to be represented on that committee. International organizations, governmental and non-governmental, in liaison with ISO, also take part in the work. ISO collaborates closely with the International Electrotechnical Commission (IEC) on all matters of electrotechnical standardization.

The procedures used to develop this document and those intended for its further maintenance are described in the ISO/IEC Directives, Part 1. In particular, the different approval criteria needed for the different types of ISO document should be noted. This document was drafted in accordance with the editorial rules of the ISO/IEC Directives, Part 2 (see www.iso.org/directives).

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This document was prepared by Technical Committee ISO/TC206, *Fine ceramics*.

A list of all parts in the ISO 5618 series can be found on the ISO website.

Any feedback or questions on this document should be directed to the user's national standards body. A complete listing of these bodies can be found at www.iso.org/members.html.

Introduction

GaN is a direct transition type of wide-bandgap semiconductor with superior physical properties, such as a higher breakdown electric field, saturated electron drift velocity, and thermal conductivity, compared with Si. GaN is expected to be applied not only in light-emitting devices that have been in practical use for a long time, such as ultraviolet and blue LDs and LEDs, but also in power devices that perform power conversion with high efficiency. In particular, the characteristics of GaN power devices are utilized in the fields of photovoltaics, automobiles, railways (electric motors and linear motors), communication base stations, and microwave power transmission.

The single-crystal GaN or single-crystal GaN film is the base material of many devices. However, the surface of the single-crystal GaN or single-crystal GaN film contains many dislocations that are introduced during crystal growth and defects introduced during wafer processing. These dislocations and/or defects cause a decrease in luminous efficiency for a light-emitting device and a decrease in performance and reliability for a power device. In particular, given the practical applications and market expansion of power devices that apply a high voltage and high current, it is indispensable to supply single-crystal GaN substrates and single-crystal GaN films with a low-density of dislocations and defects. Therefore, it is essential to have an international standard that defines and classifies the types of dislocations and processing-introduced defects that exist on the surface as an index for assessing the quality of a single-crystal GaN substrate or a single-crystal GaN film and determines the density of these dislocations and defects.

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Fine ceramics (advanced ceramics, advanced technical ceramics) Test method for GaN crystal surface defects

Part 2 Method for determining etch pit density

1 Scope

This document describes a method for determining the etch pit density, which is used to detect dislocations and processing-introduced defects that occur on single-crystal GaN substrates or single-crystal GaN films.

It is applicable to the defects specified in ISO 5618-1 from among the defects exposed on the surface of the following types of GaN substrates or films: single-crystal GaN substrate; single-crystal GaN film formed by homoepitaxial growth on a single-crystal GaN substrate; or single-crystal GaN film formed by heteroepitaxial growth on a single-crystal Al₂O₃, SiC, or Si substrate.

It is applicable to defects with an etch pit density of $\leq 7 \times 10^7$ cm⁻².

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

ISO 5618-1, *Fine ceramics (advanced ceramics, advanced technical ceramics) — Test method for GaN crystal surface defects — Part 1: Classification of defects*

ISO 19606, *Test method for surface roughness of fine ceramic films by atomic force microscopy*

ISO 21920-2:2021, *Geometrical Product Specifications (GPS) — Surface texture: Profile method — Part 2: Terms, definitions and surface texture parameters*

ISO/IEC 17025, *General requirements for the competence of testing and calibration laboratories*

3 Terms and definitions

4 s

For the purposes of this document, the terms and definitions given in ISO 5618-1 and the following apply.

ISO and IEC maintain terminology databases for use in standardization at the following addresses:

— ISO Online browsing platform: available at <https://www.iso.org/obp>

— IEC Electropedia: available at <https://www.electropedia.org/>

3.1

primary orientation flat

flat part of the surrounding area of the substrate that is used to indicate the crystal orientation

3.2

secondary flat

flat part of the surrounding area of the substrate that is shorter than the primary orientation flat

3.3

optical microscope

microscope used to magnify and observe an object by using visible light

EXAMPLE-___white, monochromatic, or laser light

3.4

numerical aperture

NA

sine of the vertex angle of the largest cone of meridional rays that can enter or leave an optical system or element, multiplied by the refractive index of the medium in which the vertex of the cone is located

[SOURCE: IEC 60050-731-03-85]

3.5

pixels per inch

ppi

individual pixels in a line or column of a digital image within a span of 25,4 mm (1 inch)

[SOURCE: ISO/IEC 39794-5:2019]

3.6

chemical mechanical polishing

CMP

polishing that flattens the surface of a semiconductor substrate by means of a chemical action and a machine action

3.7

catalyst-referred etching

processing that induces chemical etching only on the reference plane to flatten the surface of a semiconductor substrate with high accuracy but without disrupting its crystallinity

3.8

etch pit

inverted hexagonal pyramidal or oval dent generated by etching

3.9

length of longer diagonal

length of the line that connects the opposite corners of the hexagonal shape at the bottom of the inverted hexagonal pyramid

3.10

coefficient of variation

relative variation calculated by dividing the standard deviation by the arithmetic mean

**3.11
multiphoton excitation microscope**

microscope used to observe the light emission distribution caused by simultaneous multiphoton absorption

**3.12
photoluminescence**

PL
luminescence caused by optical excitation

**3.13
cathodoluminescence**

CL
luminescence caused by electronic excitation

**3.14
reference sample**

sample manufactured by the same manufacturer or using the same method as that of the test sample

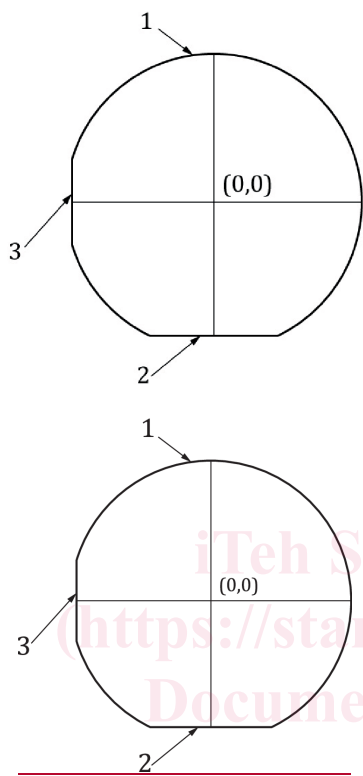
4.5 Principle

When a single-crystal GaN or single-crystal GaN film is immersed in etchant, etch pits are formed at locations where dislocations or processing-introduced defects are cropped out on the (0001)Ga polar face. These etch pits are formed because the etching speed at locations where dislocations or processing-introduced defects are cropped out on the (0001)Ga polar face is faster than that at locations where there are no dislocations. For threading dislocations, etch pits in the shape of an inverted hexagonal pyramid that consists of six faces of $\{1\bar{1}0n\}$ and has a vertex are formed. Dislocation-derived pits have this vertex. For basal plane dislocations, an oval etch pit is generated. In this case, one side is narrower and deeper than the other side.

Etch pits shaped like an inverted hexagonal truncated pyramid or etch pits shaped like a bowl that are caused by a corrupted inverted hexagonal truncated pyramid, are derived from pits caused by processing. Linear grooves are derived from scratches and latent scratches caused by processing.

5.6 Definition of substrate in-plane position

With the (0001)Ga polar face facing up, place the primary orientation flat at the bottom, as shown in [Figure 1](#). After that, draw a perpendicular bisector for the primary orientation flat. The position of the 1/2-centre side along the perpendicular bisector starting from the intersection point between the perpendicular bisector and the substrate upper outline is set as the origin point (0,0). The substrate in-plane position is expressed as (x, y) in units of millimetres (mm).



Key

- 1 substrate
- 2 primary orientation flat
- 3 secondary flat

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Figure 1 — Definition of substrate in-plane position

6.7 Procedures for forming an etch pit

6.17.1 Pre-treatment of a sample

Set the average surface roughness of a sample R_a , as defined in ISO 21920-2:2021, to be ≤ 1 nm. When R_a exceeds 1 nm, flatten the surface by means of a method such as chemical mechanical polishing or catalyst-referred etching. Once the surface has been flattened, record this information in the report specified in [Clause 10](#).

Measure the surface roughness in accordance with ISO 19606.