

ETSI GS ORI 002-1 V1.1.1 (2011-10)



Open Radio equipment Interface (ORI); ORI Interface Specification; Part 1: Low Layers (Release 1)

<https://standards.iteh.ai>
Document Preview

<https://standards.iteh.ai/catalog/standards/etsi/871b214b-1615-45ec-bebb-c97a0e70d6cc/etsi-gs-ori-002-1-v1-1-1-2011-10>

Disclaimer

This document has been produced and approved by the Open Radio equipment Interface (ORI) ETSI Industry Specification Group (ISG) and represents the views of those members who participated in this ISG.
It does not necessarily represent the views of the entire ETSI membership.

Reference

DGS/ORI-0002-1

Keywords

E-UTRA, interface, radio, UTRA

ETSI

650 Route des Lucioles
F-06921 Sophia Antipolis Cedex - FRANCE

Tel.: +33 4 92 94 42 00 Fax: +33 4 93 65 47 16

Siret N° 348 623 562 00017 - NAF 742 C
Association à but non lucratif enregistrée à la
Sous-Préfecture de Grasse (06) N° 7803/88

<https://standards.iteh.ai>
Document Preview

Important notice

Individual copies of the present document can be downloaded from:

<http://www.etsi.org>

The present document may be made available in more than one electronic version or in print. In any case of existing or perceived difference in contents between such versions, the reference version is the Portable Document Format (PDF). In case of dispute, the reference shall be the printing on ETSI printers of the PDF version kept on a specific network drive within ETSI Secretariat.

Users of the present document should be aware that the document may be subject to revision or change of status. Information on the current status of this and other ETSI documents is available at

<http://portal.etsi.org/tb/status/status.asp>

If you find errors in the present document, please send your comment to one of the following services:

http://portal.etsi.org/chaicor/ETSI_support.asp

Copyright Notification

No part may be reproduced except as authorized by written permission.
The copyright and the foregoing restriction extend to reproduction in all media.

© European Telecommunications Standards Institute 2011.
All rights reserved.

DECT™, PLUGTESTS™, UMTS™ and the ETSI logo are Trade Marks of ETSI registered for the benefit of its Members.
3GPP™ and LTE™ are Trade Marks of ETSI registered for the benefit of its Members and
of the 3GPP Organizational Partners.
GSM® and the GSM logo are Trade Marks registered and owned by the GSM Association.

Contents

Intellectual Property Rights	4
Foreword.....	4
1 Scope	5
2 References	5
2.1 Normative references	5
2.2 Informative references.....	6
3 Definitions, symbols and abbreviations	6
3.1 Definitions.....	6
3.2 Symbols.....	6
3.3 Abbreviations	7
4 ORI Low Layers specification compliance	7
5 Layer 1 configuration	7
5.1 General	7
5.2 Optical interface	8
5.3 Electrical interface.....	8
6 Control plane.....	9
6.1 Mapping to CPRI protocol structure	9
6.2 C&M resource allocation	9
6.3 ORI reserved area.....	10
6.3.1 PORT_ID.....	10
6.3.2 UTRA RTWP measurement report.....	11
6.4 ORI low layer reset.....	12
6.5 Data link layer for Fast C&M channel	12
7 User plane.....	13
7.1 Mapping and format of IQ data.....	13
7.1.1 E-UTRA.....	13
7.1.2 UTRA-FDD.....	13
7.1.3 E-UTRA and UTRA-FDD combined	14
8 ORI start-up sequence	14
8.1 General	14
8.2 Optical interface	14
8.3 CPRI Transition 6 in ORI.....	15
8.4 Layer 1 start-up timer value	15
8.5 CPRI State B duration in ORI.....	15
Annex A (informative): Example for topology detection based on PORT_ID.....	16
History	17