

# INTERNATIONAL STANDARD

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**Materials for circuit boards and other interconnecting structures -  
Part 3-6: Sectional specification set for unreinforced base materials clad and  
unclad - Polytetrafluoroethylene (PTFE) filled laminate sheets of defined  
flammability (vertical burning test), copper-clad**

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## INTERNATIONAL ELECTROTECHNICAL COMMISSION

**Materials for circuit boards and other interconnecting structures -  
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The text of this International Standard is based on the following documents:

Draft	Report on voting
91/2102/FDIS	91/2110/RVD

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at [www.iec.ch/members\\_experts/refdocs](http://www.iec.ch/members_experts/refdocs). The main document types developed by IEC are described in greater detail at [www.iec.ch/publications](http://www.iec.ch/publications).

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# Sample Document

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## 1 Scope

This part of IEC 61249 specifies requirements for properties of polytetrafluoroethylene (PTFE) filled unreinforced laminated sheet of a thickness 0,02 mm up to 3,2 mm, of defined flammability (vertical burning test), copper-clad.

This document is applicable to the design, manufacture, use of PTFE filled unreinforced laminated sheet of defined flammability (vertical burning test), copper-clad.

Its flame resistance is defined in terms of the flammability requirements of 8.3.

## 2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 61189-2, *Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 2: Test methods for materials for interconnection structures*

IEC 61189-2-721, *Test methods for electrical materials, printed board and other interconnection structures and assemblies - Part 2-721: Test methods for materials for interconnection structures - Measurement of relative permittivity and loss tangent for copper clad laminate at microwave frequency using split post dielectric resonator*

IEC 61189-2-803, *Test methods for electrical materials, printed board and other interconnection structures and assemblies - Part 2-803: Test methods for Z-axis expansion of base materials and printed boards*

IEC 61189-2-805, *Test methods for electrical materials, printed boards and other interconnection structures and assemblies - Part 2-805: X/Y CTE test for thin base materials by TMA*

IEC 61189-2-807, *Test methods for electrical materials, printed board and other interconnection structures and assemblies - Part 2-807: Test methods for materials for interconnection structures - Decomposition temperature ( $T_d$ ) using TGA*

IEC 61249-5-1, *Materials for interconnection structures - Part 5: Sectional specification set for conductive foils and films with or without coatings - Section 1: Copper foils (for the manufacture of copper-clad base materials)*

ISO 11014, *Safety data sheet for chemical products - Content and order of sections*

IPC-TM-650 TM 2.5.5.5, *Stripline Test for Permittivity and Loss Tangent (Dielectric Constant and Dissipation Factor) at X-Band*