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INTERNATIONAL STANDARD

**Printed electronics -
Part 403-2: Printability - Requirements for reproducibility - Basic patterns for
printing plate**

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**Printed electronics -
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Basic patterns for printing plate**

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Draft	Report on voting
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Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this International Standard is English.

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INTRODUCTION

This document provides commonly utilized design patterns for evaluating printability with focus on printing plate micro pattern resolution. Reproducibility of micro patterns in printed electronics is checked by the size and shape of line and space patterns affected by printing plate. Precision quality of the high-resolution printing plate is key to the fabrication of the fine patterning that makes up electronic circuits and components in the field of printed electronics manufacturing. Quality and reproducibility of the printing technology in electronics manufacturing help to facilitate international trade and enhanced user value in the field of printed electronics. The IEC 62899-403 series contains basic patterns to evaluate the printability of patterns from the viewpoint of printing machine, printing plate, and applications for printed electronics manufacturing. The printability is estimated by both the quality of printed patterns and the reproducibility of printing designs. On the other hand, the reproducibility of the printed pattern is achieved by estimating the fidelity from the printing design data. IEC 62899-403-1 provides commonly utilized design patterns for evaluating printability by the point of operation accuracy of the printing machine.

The quality of printed patterns is ensured by accurate measurements of their size and dimensions (2D and/or 3D). The printability of patterns can be measured with the methods provided in the IEC 62899-402 series.

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