SLOVENSKI STANDARD
oSIST prEN 50766:2026

01-marec-2026

Zahteve za kibernetsko varnost za mikroprocesorje in mikrokrmilnike, odporne proti nedovoljenim posegom

Cybersecurity requirements for tamper-resistant microprocessors and microcontrollers

Cybersicherheitsanforderungen für sichere Mikroprozessoren und Mikrocontroller mit oder ohne Anwendungsumgebung oder Betriebssystem

Exigences de cybersécurité pour les microprocesseurs et microcontrôleurs sécurisés avec ou sans environnement d'application ou système d'exploitation

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ICS:

35.030	Informacijska varnost	IT Security
35.160	Mikroprocesorski sistemi	Microprocessor systems

oSIST prEN 50766:2026 **en**

2003-01.Slovenski inštitut za standardizacijo. Razmnoževanje celote ali delov tega standarda ni dovoljeno.

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EUROPEAN STANDARD

NORME EUROPÉENNE

EUROPÄISCHE NORM

DRAFT
prEN 50766

January 2026

ICS 35.030; 35.160 -

English Version

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This draft European Standard is submitted to CENELEC members for enquiry.
Deadline for CENELEC: 2026-04-10.

It has been drawn up by CLC/TC 47X.

If this draft becomes a European Standard, CENELEC members are bound to comply with the CEN/CENELEC Internal Regulations which stipulate the conditions for giving this European Standard the status of a national standard without any alteration.

This draft European Standard was established by CENELEC in three official versions (English, French, German).
A version in any other language made by translation under the responsibility of a CENELEC member into its own language and notified to the CEN-CENELEC Management Centre has the same status as the official versions.

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Recipients of this draft are invited to submit, with their comments, notification of any relevant patent rights of which they are aware and to provide supporting documentation.

Warning : This document is not a European Standard. It is distributed for review and comments. It is subject to change without notice and shall not be referred to as a European Standard.

European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

CEN-CENELEC Management Centre: Rue de la Science 23, B-1040 Brussels

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European foreword

This document (prEN 50766:2026) has been prepared by CLC TC47X “semiconductors and trusted chips implementation”.

This document is currently submitted to the Enquiry

The following dates are proposed:

• latest date by which the existence of this document has to be announced at national level	(doa)	dav + 6 months
• latest date by which this document has to be implemented at national level by publication of an identical national standard or by endorsement	(dop)	dav + 12 months
• latest date by which the national standards conflicting with this document have to be withdrawn	(dow)	dav + 36 months (to be confirmed or modified when voting)

This document has been prepared under a standardization request addressed to CENELEC by the European Commission. The Standing Committee of the EFTA States subsequently approves these requests for its Member States.

For the relationship with EU Legislation, see informative Annex ZZ, which is an integral part of this document.

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