
Preskusne metode za električne materiale, tiskana vezja in druge povezovalne strukture in sestave - 3-302. del: Odkrivanje napak pri galvanizaciji golih tiskanih vezij z računalniško tomografijo (CT)

Test methods for electrical materials, printed board and other interconnection structures and assemblies - Part 3-302: Detection of plating defects in unpopulated circuit boards by computed tomography (CT)

Prüfverfahren für Elektromaterialien, Leiterplatten und andere Verbindungsstrukturen und Baugruppen – Teil 3-302: Computertomographisches Verfahren (CT) zur Detektion von Metallisierungsfehlern in unbestückten Leiterplatten

Méthodes d'essai pour les matériaux électriques, les cartes imprimées et autres structures d'interconnexion et ensembles - Partie 3-302: Détection des défauts de métallisation dans les cartes de circuits imprimés nus par tomographie informatisée (TI)

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English Version

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interconnection structures and assemblies - Part 3-302:
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andere Verbindungsstrukturen und Baugruppen - Teil 3-
302: Computertomographisches Verfahren (CT) zur
Detektion von Metallisierungsfehlern in unbestückten
Leiterplatten
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European Committee for Electrotechnical Standardization
Comité Européen de Normalisation Electrotechnique
Europäisches Komitee für Elektrotechnische Normung

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EN IEC 61189-3-302:2025 (E)**European foreword**

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The following dates are fixed:

- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2026-11-30
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2028-11-30

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In the official version, for Bibliography, the following note has to be added for the standard indicated:

ISO 15708-1:2024 NOTE Approved as EN ISO 15708-1:2024 (not modified)

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